

Course : MTEC1 - M.Tech. (VLSI Design and Embedded Systems)
College : 1002 - BGS Institute of Technology, BG Nagara
Student's Name : NAVEEN B K



Reg. No. 2 0 M V L 0 0 4

Student Id. 1 8 0 0 2 9 2 1

Exam Center : 1002 - BGS Institute of Technology, BG Nagara



SUBJECTS / PAPERS APPEARING

Sl. No.	Sem	Sub Code	Subject Title	QP Code	Date and Time	Answer Book No.	Invigilator Signature
1	I	A2010	Digital VLSI Design [Theory]	53111	19/07/2021 - 09:30 am - 12:30 pm		
2	I	A2020	Advanced Embedded System [Theory]	53112	22/07/2021 - 09:30 am - 12:30 pm		
3	I	A2030	System Verilog [Theory]	53113	24/07/2021 - 09:30 am - 12:30 pm		
4	I	A2040	ASIC Design [Theory]	53114	26/07/2021 - 09:30 am - 12:30 pm		
5	I	A2050	Research Methodology and IPR [Theory]	46115	28/07/2021 - 09:30 am - 12:30 pm		
6	I	AP040	VLSI and ES laboratory - I [Practical]		-		

Student Signature



REGISTRAR (Evaluation)

I certify that this candidate is eligible to appear for the current examination

Date: July 16, 2021, 7:14 am

Principal Signature with seal