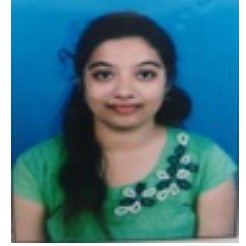


Course : MTEC1 - M.Tech. (VLSI Design and Embedded Systems)
College : 1002 - BGS Institute of Technology, BG Nagara
Student's Name : LEKHA SHREE M K



Reg. No.	2	0	M	V	L	0	0	3	Student Id.	1	8	0	0	2	9	2	0
-----------------	---	---	---	---	---	---	---	---	--------------------	---	---	---	---	---	---	---	---


Exam Center : 1002 - BGS Institute of Technology, BG Nagara



SUBJECTS / PAPERS APPEARING

Sl. No.	Sem	Sub Code	Subject Title	QP Code	Date and Time	Answer Book No.	Invigilator Signature
1	I	A1030	System Verilog [Theory]	53103	-		
2	I	A1050	Research Methodology and IPR [Theory]	46105	-		
3	I	A1020	Advanced Embedded System [Theory]	53102	-		
4	I	A1040	ASIC Design [Theory]	53104	-		
5	I	A1010	Digital VLSI Design [Theory]	53101	-		
6	I	AP010	VLSI and ES laboratory - I [Practical]		30/07/2021 - 09:30 am - 12:30 pm		

Student Signature



REGISTRAR (Evaluation)

I certify that this candidate is eligible to appear for the current examination

Date: July 16, 2021, 9:19 am

Principal Signature with seal